AMENDMENTS TO THE SPECIFICATION

The following is a marked up version of each replacement paragraph and/or section of the specification in which underlines indicate insertions and strikethroughs indicate deletions.

Please replace the eighth paragraph on page 5 with the following paragraph:

Fig. 9 is a ~~Figs. 9A & B are two~~ state diagrams which ~~in each case~~ shows specific system behavior for the vacuum test ~~and the scan test~~;

Please replace the second paragraph on page 9 with the following paragraph:

Events having the same name occur synchronously in machines in which the respective event is defined. In the present exemplary embodiment, the event "scan" occurs if the state machine of the system behavior (compare Figure 7) is in state "0" or, respectively, the state machine of the specific system behavior according to Figure 8 is in state 801 or in state 802 and if the state machine of the specific system behavior according to Figure 9 ~~figures 9A and 9B~~ is in state 903 ~~or 913, respectfully~~.

Please replace the sixth paragraph on page 9 with the following paragraph:

After the system behavior has been identified, the specific system behavior is determined which relates to a behavior of the complete process with regard to the task to be controlled. The associated state machine for the specific system behavior of the error recovery is shown in Figure 8. In Figure 8, there are marked states 801 to 807, where state 801 is simultaneously the start and an end state of the state machine. The specific system behavior "error recovery" can be terminated in each case in states 801 and 807. Items 803-806 indicate the following states: 803, system is recovering; 804, system has recovered; 805, system has received operator input; and 806, system is returning to initial state.

Please replace the second full paragraph on page 10 with the following paragraph:

According to the above statements, the validation is then performed. To this end, a number of iterations which, finally, lead to the solution according to Figure 7 to ~~Figures 9A and 9B~~ Figure 9 are shown according to Figure 2 (compare transition from validation 202 to structuring 201: iteration).

Please replace the third full paragraph on page 10 with the following paragraph:

Figures 8, ~~9A and 9B~~ and 9 show the controlled specific system behavior corresponding to the predetermined functionality of the complete process. Figures ~~9A and 9B~~ illustrates states 901 and 907 ~~and 911 to 917 respectively~~. Items 902-906 correspond to item numbers 402-406, respectively. It is noted that item numbers 902-906 describe the following states: 902, turret head 101 is being turned; 904, a vacuum is being applied; and 906, a counter has been incremented.

Three tasks have been identified which are executed in parallel: error recovery, scan test, and vacuum test. The error recovery, in particular, is only activated if both the scan test 104 and the vacuum test 103 occur in a marked state (compare state 807 in Figure 8 or states 907, ~~917~~ in Figures ~~9A & B~~ respectively). The scan test 104 and the vacuum test 103 are only activated if the error recovery is in the initial state (compare state 0 or 801 in Figure 8).

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